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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Takashi KOBAYASHI et al.

U.S. Patent No.: 6,894,319 B2

Issue Date: May 17, 2005

Serial No.: 10/020,573

Filed: October 31, 2001

Title: VERTICAL INSULATED GATE  
SEMICONDUCTOR DEVICE

Attorney Docket No.: FUJI:200

**Attention Certificate of Corrections Branch**

COMMISSIONER FOR PATENTS

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BY: MARCA ROSSI

**Certificate**

JUN 13 2005

**of Correction**

**REQUEST FOR CERTIFICATE OF CORRECTION**

Sir:

Applicants request that a Certificate of Correction be issued to correct errors that appear in the above-referenced patent due to a mistake made on the part of the Office, namely, the Title is incorrect and the dates of the first two listed Foreign Application Priority documents are incorrect. Applicants request that a Certificate of Correction be issued to show the Title as indicated on the Notice of Allowance and show the Priority documents with the correct Priority dates.

However, in the event that it should be determined that the error is not the fault of the Office, the Commissioner is authorized to charge deposit account 18-2056 any fees associated with the issuance of the Certificate of Correction.

Respectfully submitted,

ROSSI, KIMMS & McDOWELL LLP

DATE

06/07/05

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JUN 13 2005

**UNITED STATES PATENT AND TRADEMARK OFFICE  
CERTIFICATE OF CORRECTION**Page 1 of 1

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APPLICATION NO.: 10/020,573

ISSUE DATE : May 17, 2005

INVENTOR(S) : Takashi KOBAYASHI et al.

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

The title shown is incorrect and should read:

--VERTICAL INSULATED GATE SEMICONDUCTOR DEVICE--

In the Section labeled Foreign Application Priority Data, the dates of the first two listed Priority Applications are incorrect and should be shown as follows:

--October 31, 2000 JP 2000-331840

November 28, 2000 JP 2000-361106--

**MAILING ADDRESS OF SENDER (Please do not use customer number below):**

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